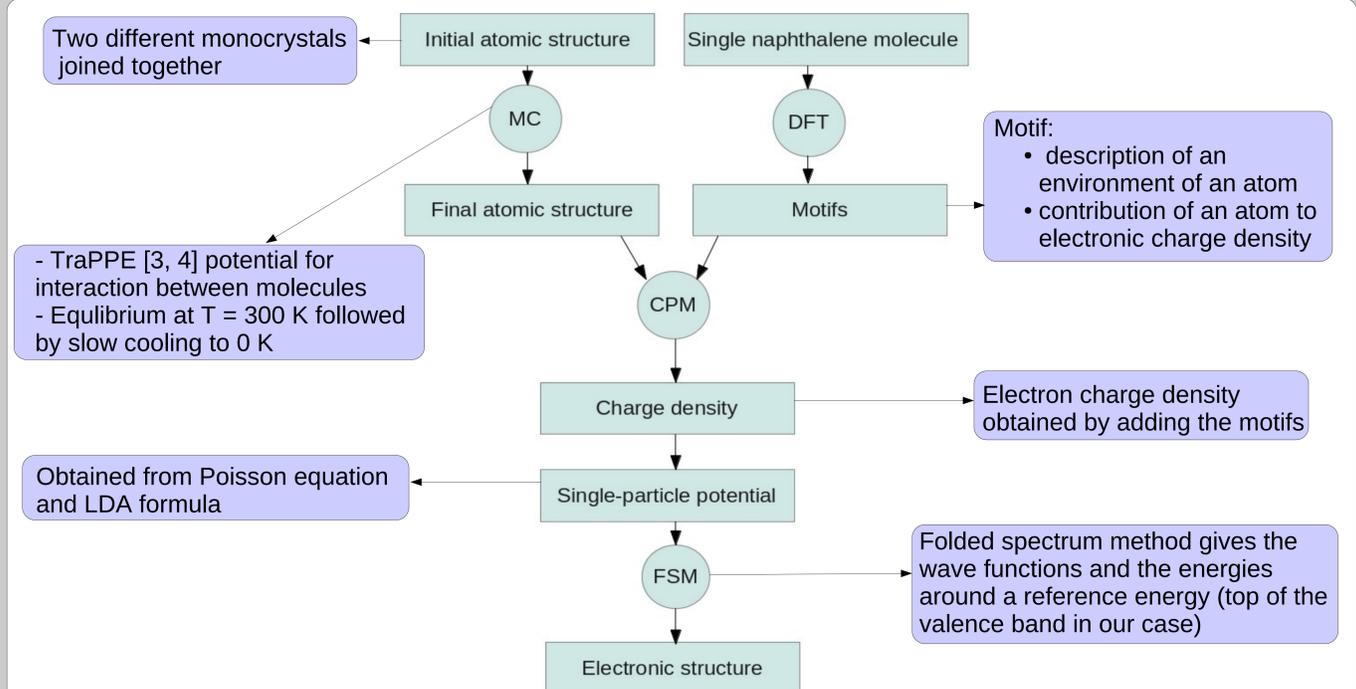


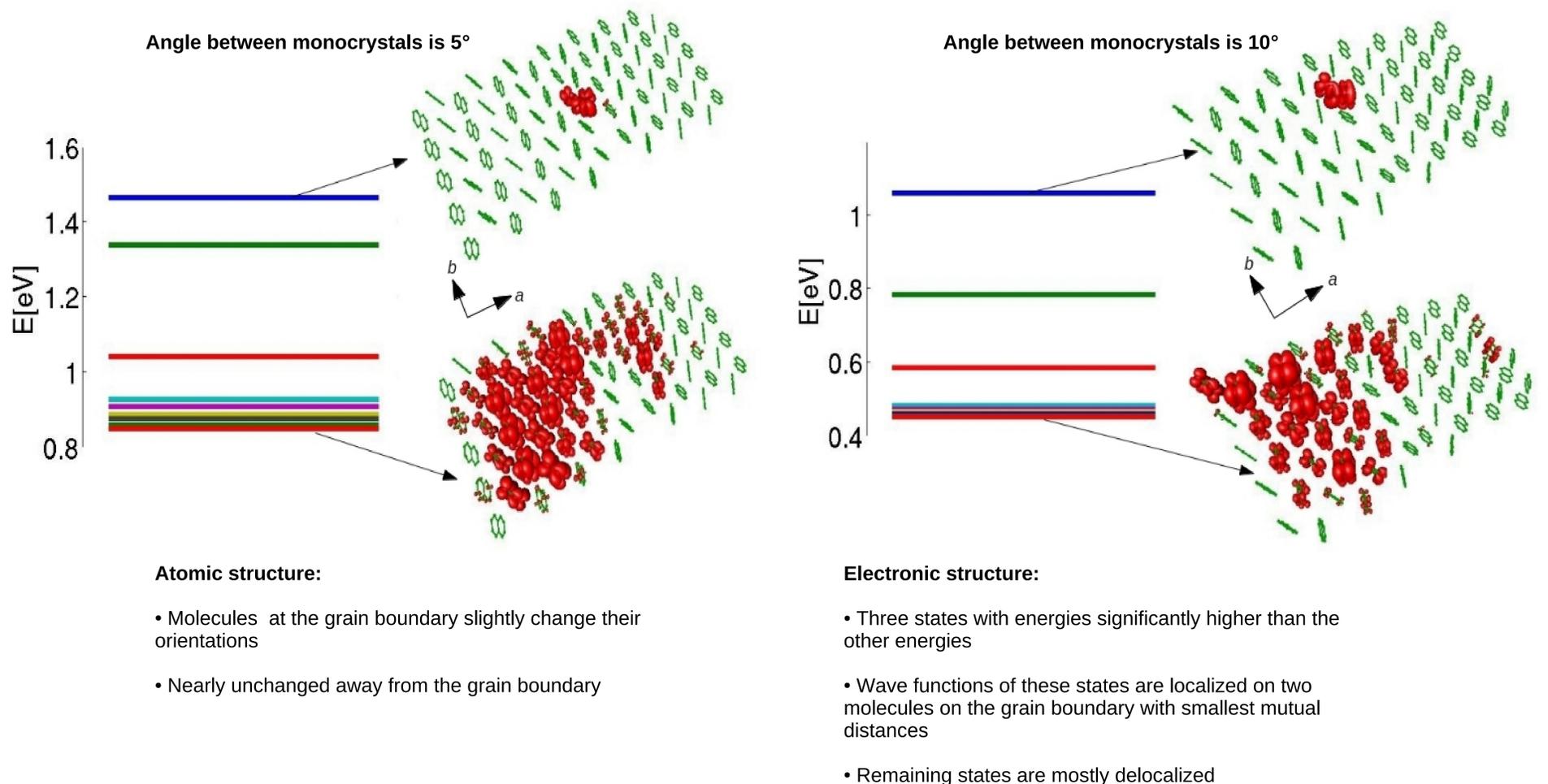
Introduction

- Organic semiconductors are promising materials for LEDs, transistors and solar cells
- Crystalline organic semiconductors form polycrystals
- Grain boundaries:
 - contact surfaces between different monocrystals
 - produce trap states for charge carriers
 - affect the performance of the devices
- We present a method for investigation of the role of grain boundaries in organic semiconductors
- Atomic structure: Monte Carlo algorithm
- Electronic structure: charge patching method [1] and folded spectrum method [2]

Model description



Results



Discussion

- Grain boundaries in organic polycrystals:
 - have small impact on atomic structure
 - introduce localized states in the energy band gap of a material
- Localized states at the grain boundary are trap states for charge carriers
- In the devices which operate in low carrier concentration regime, such as LEDs and solar cells, traps reduce carrier mobility
- Traps broaden absorption and emission spectrum

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